Form PTO-1449	ATTORNEY DOCKET NO. AUS920030555US1	SERIAL NO. N̶ ̶Ꞌ̶A̶s̶s̶i̶g̶n̶-̶d̶ 10/757, 227
LIST OF PRIOR ART CITED BY APPLICANT *(Use several sheets if necessary)*	APPLICANT **DeWitt, Jr. et al.**	
	FILING DATE 1/14/04	GROUP ART UNIT N̶o̶t̶ ̶A̶s̶s̶i̶g̶n̶e̶d̶ 2185

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EXAMINER INITIAL		DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
A.S.	AA	5,103,394	Apr. 7, 1992	Blasciak	395/575	Dec. 21, 1989
A.S.	AB	6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999
A.S.	AC	6,480,938 B2	Nov. 12, 2002	Vondran, Jr.	711/125	Dec. 15, 2000
A.S.	AD	6,430,741 B1	Aug. 6, 2002	Mattson, Jr. et al.	717/154	Feb. 26, 1999
A.S.	AE	6,189,141 B1	Feb. 13, 2001	Benitez et al.	717/4	May 4, 1998
A.S.	AF	5,930,508	Jul. 27, 1999	Faraboschi et al.	395/706	Jun. 9, 1997
A.S.	AG	6,351,844 B1	Feb. 26, 2002	Bala	717/4	Nov. 5, 1998
A.S.	AH	6,324,689 B1	Nov. 27, 2001	Lowney et al.	717/9	Sep. 30, 1998
A.S.	AI	6,442,585 B1	Aug. 27, 2002	Dean et al.	709/108	Nov. 26, 1997
A.S.	AJ	5,774,724	Jun. 30, 1998	Heisch	395/704	Nov. 20, 1995
A.S.	AK	5,987,250	Nov. 16, 1999	Subrahmanyam	395/704	Aug. 21, 1997
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A.S.	AO	6,101,524	Aug. 8, 2000	Choi et al.	709/102	Oct. 23, 1997
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A.S.	AZ	6,070,009	May 30, 2000	Dean et al.	395/704	Nov. 26, 1997
A.S.	BA	5,966,537	Oct. 12, 1999	Ravichandran	395/709	May 28, 1997

DATE CONSIDERED 2/7/06	EXAMINER

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F rm PTO-1449	ATTORNEY DOCKET NO. AUS920030555US1	SERIAL NO. ~~Not Assigned~~ 16/757,227
LIST OF PRIOR ART CITED BY APPLICANT *(Use several sheets if necessary)*	APPLICANT **DeWitt, Jr. et al.**	
	FILING DATE 1/14/04	GROUP ART UNIT ~~Not Assigned~~ 2185

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EXAMINER INITIAL		DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
A.S.	BB	2002/0129309	Sep. 12, 2002	Floyd et al.	714/724	Dec. 18, 2000
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A.S.	BH	JP2000029731	Dec. 8, 1999	Japan	G06F 9/38		X
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	BJ	~~Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.~~
A.S.	BK	Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.
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A.S.	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performanc Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.

RELATED PATENT APPLICATIONS

EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
A.S.	BP	09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999
A.S.	BQ	08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Pr cessing System	Oct. 2, 1995

DATE CONSIDERED 2/7/06	EXAMINER Arpan Savla

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FORM PTO-1449	ATTORNEY DOCKET NO. AUS920030555US1	SERIAL NO. 10/757,227
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)		

APPLICANT DeWitt, Jr. et al.		
FILING DATE **January 14, 2004**	GROUP ART UNIT ~~2188~~ 2185	

RELATED PATENT APPLICATIONS

EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
A.S.	AA	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
A.S.	AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
A.S.	AC	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
A.S.	AD	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
A.S.	AE	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
A.S.	AF	10/675,751 / AUS920030482US1	DeWitt, Jr. et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
A.S.	AG	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
A.S.	AH	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
A.S.	AI	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003
A.S.	AJ	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
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A.S.	AL	10/757,250 / AUS920030488US1	Levine et al.	Method and Apparatus for Maintaining Performance Monitoring Structures in a Page Table for use in Monitoring Performance of a Computer Program	Jan. 14, 2004
A.S.	AM	10/757,192 / AUS920030543US1	DeWitt, Jr. et al.	Method and Apparatus for Providing Pre and Post Handlers for Recording Events	Jan. 14, 2004
A.S.	AN	10/757,249 / AUS920030554US1	DeWitt, Jr. et al.	Method and Apparatus for Identifying False Cache Line Sharing	Jan. 14, 2004
A.S.	AO	10/757,197 / AUS920030556US1	DeWitt, Jr. et al.	Method and Apparatus for Optimizing Code Execution Using Annotated Trace Information having Performance Indicator and Counter Information	Jan. 14, 2004

DATE CONSIDERED 2/7/06	EXAMINER Arpan Savla

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